

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1-2. (Canceled)

3. (Currently amended) The display device as set forth in claim [[2]] 8, wherein: the other circuit is a circuit for driving the scanning signal lines or the data signal lines.

4. (Currently amended) The display device as set forth in claim [[2]] 8, wherein: the first signal is supplied to the driving circuit and the other circuit from a common input terminal and through a common signal line.

5. (Currently amended) The display device as set forth in claim [[2]] 8, wherein: the first and second signals are clock signals of plural systems, respectively.

6. (Currently amended) The display device as set forth in claim [[2]] 8, wherein: the first and second signals are digital image signals constituted of a plurality of bits, and are divided into at least two bit groups.

7. (Currently amended) The display device as set forth in claim [[2]] 8, wherein: the wiring load adjustment section adjusts time constants of the respective wirings of the first and second signals.

8. (Previously presented) A display device, comprising:

a scanning signal line driving circuit for driving scanning signal lines;

a data signal line driving circuit for driving data signal lines intersecting the scanning signal lines,

at least one of the scanning signal line driving circuit and the data signal line driving circuit being supplied with at least first and second signals, the first signal being supplied in parallel to other circuit than the driving circuit supplied with the first and second signals,

the display device further comprising wiring load adjustment section for equalizing wiring load of the second signal which is supplied to the driving circuit, and wiring load of the first signal which is supplied in parallel to the driving circuit and the other circuit; wherein:

the scanning signal lines and the data signal lines are formed on a substrate, and a liquid crystal layer is held between the substrate and a substrate having a counter electrode,

the wiring load adjustment section uses the liquid crystal layer as a dielectric substance, and is constituted of dummy wiring connected to the wiring of the second signal which is supplied to the driving circuit and a liquid crystal layer on the dummy wiring, and the counter electrode.

9. (Original) The display device as set forth in claim 8, wherein: the dummy wiring is formed in a fanfold shape on a vacant area, which is an area holding the liquid crystal layer on the substrate having a counter electrode, and being provided as a part of a display section but is not involved in image display, the vacant area being closer to an end portion of the substrate than the data signal line driving circuit.

10. (Original) The display device as set forth in claim 8, wherein: the dummy wiring is formed in a plate shape to be in parallel with the counter electrode.

11. (Original) The display device as set forth in claim 8, wherein: the dummy wiring is formed in a periphery of a display section involved in image display.

12. (Previously presented) A display device, comprising:
a scanning signal line driving circuit for driving scanning signal lines;
a data signal line driving circuit for driving data signal lines intersecting the scanning signal lines,

at least one of the scanning signal line driving circuit and the data signal line driving circuit being supplied with at least first and second signals, the first signal being supplied in parallel to other circuit than the driving circuit supplied with the first and second signals,

the display device further comprising wiring load adjustment section for equalizing wiring load of the second signal which is supplied to the driving circuit, and wiring load of the first signal which is supplied in parallel to the driving circuit and the other circuit; wherein:

the scanning signal lines and the data signal lines are formed on a substrate where an interlayer insulation film and a conductive film are formed, and

the wiring load adjustment section uses the interlayer insulation film as a dielectric substance, and is constituted of dummy wiring connected to the wiring of the second signal supplied to the driving circuit, the interlayer insulation film, and the conductive film.

13. (Original) The display device as set forth in claim 12, wherein: the dummy wiring is formed in a periphery of a display section involved in image display.

14. (Previously presented) A display device, comprising:
a scanning signal line driving circuit for driving scanning signal lines;
a data signal line driving circuit for driving data signal lines intersecting the scanning signal lines,

at least one of the scanning signal line driving circuit and the data signal line driving circuit being supplied with at least first and second signals, the first signal being supplied in parallel to other circuit than the driving circuit supplied with the first and second signals,

the display device further comprising wiring load adjustment section for equalizing wiring load of the second signal which is supplied to the driving circuit, and wiring load of the first signal which is supplied in parallel to the driving circuit and the other circuit; wherein:

the scanning signal lines and the data signal lines have a thin film transistor for each intersection, and

the wiring load adjustment section uses layers for constituting a gate insulation film of a thin film transistor as a dielectric substance, and is constituted of dummy wiring connected to the wiring of the second signal supplied to the driving circuit, and layers for constituting a gate insulation film and a semiconductor layer of a thin film transistor stacked on the dummy wiring.

15. (Original) The display device as set forth in claim 14, wherein: the dummy wiring is formed in a periphery of a display section involved in image display.

16. (Currently amended) The display device as set forth in claim [[2]] 8, wherein: the other circuit is a pre-charging circuit for carrying out pre-charging of the data signal lines.

17. (Currently amended) The display device as set forth in claim [[2]] 8, wherein: the wiring load adjustment section is provided in the scanning signal line driving circuit.

18. (New) The display device as set forth in claim 12, wherein: the other circuit is a circuit for driving the scanning signal lines or the data signal lines.

19. (New) The display device as set forth in claim 12, wherein: the first signal is supplied to the driving circuit and the other circuit from a common input terminal and through a common signal line.

20. (New) The display device as set forth in claim 12, wherein: the first and second signals are clock signals of plural systems, respectively.

21. (New) The display device as set forth in claim 12, wherein: the first and second signals are digital image signals constituted of a plurality of bits, and are divided into at least two bit groups.

22. (New) The display device as set forth in claim 12, wherein: the wiring load adjustment section adjusts time constants of the respective wirings of the first and second signals.

23. (New) The display device as set forth in claim 14, wherein: the other circuit is a circuit for driving the scanning signal lines or the data signal lines.

24. (New) The display device as set forth in claim 14, wherein: the first signal is supplied to the driving circuit and the other circuit from a common input terminal and through a common signal line.

25. (New) The display device as set forth in claim 14, wherein: the first and second signals are clock signals of plural systems, respectively.

26. (New) The display device as set forth in claim 14, wherein: the first and second signals are digital image signals constituted of a plurality of bits, and are divided into at least two bit groups.

27. (New) The display device as set forth in claim 14, wherein: the wiring load adjustment section adjusts time constants of the respective wirings of the first and second signals.